PTO/SB/(08-03) Approved for use through 07/31/2006. OMB 0851-0031 U.S. Patent and Trademark Office. U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. C mplet if Kn wn Substitute for form 1449/PTO 10/757,192 **Application Number** January 14, 2004 Filing Date First Named Inventor DeWitt, Jr. et al. MATION DISCLOSURE Raymond N. Phan **Examiner Name** ATEMENT BY APPLICANT 2111 **Art Unit** AUS920030543US1 Attorney Docket No. Name of Patentee or Pages, Columns, Lines, Where **Document Number Publication Date Examiner** MM-DD-YYYY **Applicant of Cited Document** Relevant Passages or Relevant Initials Figures Appear Gupta et al. 5,710,881 01-20-1998 AA 10-13-1998 Baylor et al. 5,822,763 AB Mandyam et al. 07-27-1999 5,928,334 AC Baylor et al. 07-25-2000 AD 6,094,709 Mandyam et al. 09-04-2001 6,285,974 B1 ΑE Mithal et al. 6,636,950 B1 10-21-2003 AF 2004/0205302 A1 10-14-2004 Cantrill AG FOREIGNIPATENTEDOCUMENTS Pages, Columns, Lines, Wher **Publication Date** Examiner Name of Patentee or Relevant Passages or Relevant Foreign Patent Document MM-DD-YYYY Initial **Applicant of Cited Document** Figures Appear AND THE PROPERTY OF THE PROPER Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item Examiner (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), Initial publisher, city and/or country where published. TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2<sup>nd</sup> Edition, pp. 10-12. AH

Examiner:

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RAYMOND PHAN

Transactions on Computers, Vol. 43, No. 6, pp. 651-663.

Date Considered:

5-20-06

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE

ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.